

AMENDMENTS TO THE SPECIFICATION

Please amend the paragraph that begins on page 13, line 19, as follows:

The homogeneous structure of processors and software cells of system **101** also avoids many of the problems of today's heterogeneous networks. For example, inefficient programming models which seek to permit processing of applications on any ISA using any instruction set, e.g., virtual machines such as the ~~Java~~ JAVA™ virtual machine, are avoided. System **101**, therefore, can implement broadband processing far more effectively and efficiently than today's networks.